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U.S. Patent Documents

Foreign Patent or Published Foreign Patent Application

Other Documents

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4/27/05



Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty. Docket No.	Application No.:
	NVIDP322/P001314	10/049,972
	Applicant:	
	Michael Ward Johnson et al.	
Filing Date:	Group Art Unit:	
02/13/2002	2661 2155	

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
AB	A	5,012,489	4/30/1991	Burton et al.	375	8	11/7/1998
	B	5,161,193	11/3/1992	Lampson et al.	380	49	6/29/1990
	C	5,163,131	11/10/1992	Row et al.	395	200	9/8/1989
	D	5,303,344	4/12/1994	Yokoyama et al.	395	200	2/25/1991
	E	5,307,413	4/26/1994	Denzer	380	49	7/19/1991
	F	5,426,694	6/20/1995	Herbert	379	242	10/8/1993
	G	5,430,727	7/4/1995	Callon	370	85.13	5/19/1994
	H	5,440,551	8/8/1995	Suzuki	370	60	1/4/1994
	I	5,485,460	1/16/1996	Schrier et al.	370	94.1	8/19/1994
	J	5,495,480	2/27/1996	Yoshida	370	60	6/21/1994
	K	5,499,353	3/12/1996	Kadlec et al.	395	445	6/20/1995

Foreign Patent or Published Foreign Patent Application

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	L						Yes	No

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
AB	M	Chesson, Greg, "The Protocol Engine Project" Technology Focus September 1987
AB	N	Chesson, Greg, "Proceedings of the Summer 1987 USENIX Conference" USENIX Association June 8-12, 1987
AB	O	G. Chesson and L. Green, "XTP Protocol Engine VLSI for Real-Time LANS" EFOC/LAN June 29-July 1, 1968
AB	P	Stevens, Richard W., "TCP/IP Illustrated Volume" Addison-Wesley Professional Computing Series
AB	Q	Abbot, Mark B., and Peterson, Larry L., "Increasing Network Throughput by Integrating Protocol Layers" IEEE 1993
Examiner <i>Alicia Baturoy</i>		Date Considered <i>10/11/2005</i>

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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
AB	A	5,513,324	4/30/1996	Dolin, Jr. et al.	395	200.18	3/7/1994
	B	5,519,704	3/21/1996	Farinacci et al.	370	85.13	4/21/1994
	C	5,546,453	8/13/1996	Herbert	379	242	6/7/1995
	D	5,566,170	10/15/1996	Bakke et al.	370	60	12/29/1994
	E	5,577,105	11/19/1996	Baum et al.	379	93	3/11/1994
	F	5,577,172	11/19/1996	Vatland et al.	395	114	8/31/1994
	G	5,577,237	11/19/1996	Lin	395	555	1/23/1995
	H	5,598,410	1/28/1997	Stone	370	469	12/29/1994
	I	5,619,650	4/8/1997	Bach et al.	395	200.01	9/21/1995
	J	5,625,678	4/29/1997	Blomfield-Brown	379	93	5/24/1995
	K	5,625,825	4/29/1997	Rostoker et al.	395	730	10/21/1993

Foreign Patent or Published Foreign Patent Application

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							Yes	No
	L							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
AB	M	Wright, Maury, "Low-Cost Control LANs Add Automation to Homes, Autos, and Offices" EDN July 20, 1992
AB	N	Preston, David J., "Internet Protocols Migrate to Silicon For Networking Devices" Electronic Design April 14, 1997
	O	Muller, Raimund, "LON - das universelle Netzwerk" Elektronik 22/1994
AB	P	Rang, Michael ad Tantawy, Ahmed, "A Design Methodology for Protocol Processors" IEEE 1995
Examiner	Alicia Baturay	
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Not in English

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U.S. Patent Documents

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AB	A	5,634,015	5/27/1997	Chang et al.	395	309	10/4/1994
	B	5,636,371	6/3/1997	Yu	395	500	6/7/1995
	C	5,640,394	6/17/1997	Schrier et al.	370	389	1/16/1996
	D	5,663,951	9/2/1997	Danneels et al.	370	230	6/6/1995
	E	5,666,362	9/9/1997	Chen et al.	370	420	7/25/1995
	F	6,575,507	10/7/1997	Bobo, II	364	514R	4/28/1995
	G	5,687,314	11/11/1997	Osman et al.	395	200.01	6/9/1992
	H	5,696,899	12/9/1997	Kalwitz	395	200.1	11/18/1992
	I	5,699,350	12/16/1997	Kraslavsky	370	254	10/6/1995
	J	5,701,316	12/23/1997	Alferness et al.	371	53	8/31/1995
	K	5,727,149	3/10/1998	Hirata et al.	395	200.8	12/21/1995

Foreign Patent or Published Foreign Patent Application

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							Yes	No
	L							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
AB	R	Banks, David and Prudence, Michael, "A High-Performance Network Architecture for a PA-RISC Workstation" IEEE Journal Vol. II, No. 22 February 1993
AB	S	Steenkiste, Peter, "A High-Speed Network Interface for Distributed-Memory Systems: Architecture and Applications" ACM Transactions on Computer Systems, Vol. 15, No. 1 February 1997
AB	T	Doumenis, Gr.A., Konstantoulakis, G.E., Reisis, D.I. and Stassinopoulos, G.I. "A Personal Computer Hosted Terminal Adapter For The Broadband Integrated Services Digital Network and Applications" National Technical University of Athens, Greece
Examiner	<i>Alicia Baturay</i>	
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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
AB	A	5,734,865	3/31/1998	Yu	395	500	6/27/1995
	B	5,748,905	5/5/1998	Hauser et al.	395	200.79	8/30/1996
	C	5,754,540	5/19/1998	Liu et al.	370	315	7/18/1995
	D	5,761,281	6/2/1998	Baum et al.	379	93.29	7/30/1996
	E	5,790,546	8/4/1998	Dobbins et al.	370	400	12/4/1995
	F	5,790,676	8/4/1998	Ganesan et al.	380	23	9/23/1997
	G	5,802,278	9/1/1998	Isfeld et al.	395	200.02	1/23/1996
	H	5,802,306	9/1/1998	Hunt	395	200.58	10/31/1995
	I	5,805,816	9/8/1998	Picazo, Jr. et al.	395	200.53	1/28/1997
	J	5,809,235	9/15/1998	Sharma et al.	395	200.6	3/8/1996
	K	5,815,516	9/29/1998	Aaker et al.	371	53	4/5/1996

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	L							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
AB	R	Womack, Lucas; Mraz, Ronald; Mendelson, Abraham, "A Study of Virtual Memory MTU Reassembly withing the PowerPC Architecture" IEEE 1997
AB	S	Steenkiste, Peter, "A Systematic Approach to Host Interface Design for High-Speed Networks" IEEE March 1994
AB	T	Wittie, Larry D., Ma, Fanyuan, "A TCP/IP Communication Subsystem in Micros" IEEE 1987

Examiner	<i>Alicia Baturay</i>	Date Considered	<i>10/11/2005</i>
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U.S. Patent Documents

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AB	A	5,818,935	10/6/1998	Maa	380	20	3/10/1997
	B	5,826,032	10/20/1998	Finn et al.	395	200.66	2/12/1996
	C	5,872,919	2/16/1999	Wakeland	395	200.6	5/7/1997
	D	5,894,557	4/13/1999	Bade et al.	395	200.58	3/29/1996
	E	5,909,546	6/1/1999	Osborne	395	200.42	3/8/1996
	F	5,920,732	7/6/1999	Riddle	395	876	7/1/1996
	G	5,935,268	8/10/1999	Weaver	714	758	6/3/1997
	H	5,937,169	8/10/1999	Connery et al.	395	200.8	10/29/1997
	I	5,941,988	8/24/1999	Bhagwat et al.	713	201	1/27/1997
	J	5,943,481	8/24/1999	Wakeland	395	200.6	5/7/1997
	K	5,974,518	10/26/1999	Nogradi	711	173	4/10/1997

Foreign Patent or Published Foreign Patent Application

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							Yes	No
	L							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
AB	R	Dalton, Chris; Watson, Greg; Banks, David; Calamvokis, Costas; Edwards, Aled; Lumley, John, "Afterburner: A Network-independent card provides architectural support for high-performance protocols" IEEE July 1993
AB	S	Gupta, Pankaj; McKeown, Nick, "Algorithms for Packet Classification" IEEE Network March/April 2001
AB	T	Clark, David D.; Romkey, John; Salwen, Howard, "An Analysis of TCP Processing Overhead" IEEE 1988

Examiner <i>Alicia Baturay</i>	Date Considered <i>10/11/2005</i>
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U.S. Patent Documents

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AB	A	5,999,974	12/7/1999	Ratcliff et al.	709	224	8/29/1997
	B	6,014,699	1/11/2000	Ratcliff et al.	709	224	8/29/1997
	C	6,034,963	3/7/2000	Minami et al.	370	401	10/31/1996
	D	6,046,980	4/4/2000	Packer	370	230	11/24/1997
	E	6,061,742	5/9/2000	Stewart et al.	709	250	10/10/1997
	F	6,076,115	6/13/2000	Sambamurthy et al.	709	250	4/24/1997
	G	6,081,846	6/27/2000	Hyder et al.	709	250	5/8/1997
	H	6,092,110	7/18/2000	Maria et al.	709	225	10/23/1997
	I	6,092,229	7/18/2000	Boyle et al.	714	748	10/9/1996
	J	6,098,188	8/1/2000	Kalmanek, Jr. et al.	714	746	6/30/1995
	K	6,101,543	8/8/2000	Alden et al.	709	229	10/25/1996

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	L							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
AB	R	Clark, David D.; Jacobson, Van; Romkey, John; Salwen, Howard, "An Analysis of TCP Processing Overhead" IEEE June 1989
AB	S	Goloi, W.K.; Behr, P. "An IPC Protocol and Its Hardware Realization For A High-Speed Distributed Multicomputer System" IEEE 1981
AB	T	Ames, Richard, "Building an Embedded Web Server from Scratch" Circuit Cellar INK February 1998
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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
AB	A	6,151,625	11/21/2000	Swales et al.	709	218	4/30/1999
	B	6,157,955	12/5/2000	Narad et al.	709	228	6/15/1998
	C	6,172,980	1/9/2001	Flanders et al.	370	401	9/11/1997
	D	6,172,990	1/9/2001	Deb et al.	370	474	11/12/1997
	E	6,173,333	1/9/2001	Jolitz et al.	709	240	1/28/1999
	F	6,182,228	1/30/2001	Boden et al.	713	201	8/17/1998
	G	6,185,619	2/6/2001	Joffe et al.	709	229	11/7/1997
	H	6,230,193	5/8/2001	Arunkumar et al.	709	218	10/31/1996
	I	6,233,626	5/15/2001	Swales et al.	710	11	10/6/1998
	J						
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Foreign Patent or Published Foreign Patent Application

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							Yes	No
	L							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
AB	R	Legg, John, "Choosing and implementing an embedded TCP/IP Stack" Electronic Product Design January 1999
AB	S	Orphanos, George; Birbas, Alexios; Petrellis, Nikos; Mountzouris, Ioannis; Malataras, Andreas, "Compensating for Moderate Effective Throughput at the Desktop" IEEE Communication Magazine April 2000
AB	T	Yocum, Kenneth G.; Chase, Jeffrey S.; Gallatin, Andrew J.; Lebeck, Alvin R., "Cut-Through Delivery in Trapeze: An Exercise in Low-Latency Messaging IEEE 1997

Examiner <i>Alvin Baturay</i>	Date Considered <i>10/11/05</i>
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							Yes	No
	L							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
AB	R	Varada, S.; Yang, Y.; Evans, D., "Data and Buffer Management in ATM Systems" TranSwitch Corporation
AB	S	Bonjour, Dominique; de Hauteclouque, Gaelle; le Moal, Jacques, "Design and Application of ATM LAN/WAN Adapters" IEEE 1998
AB	T	Kim, Chan; Jun, Jong-Jun; Park, Yeong-Ho; Lee, Kyu-Ho; Kim, Hyup-Jong, "Design and Implementation of a High-Speed ATM Host Interface Controller" Electronics and Telecommunications Research Institute, Korea
AB		Steenkiste, Peter, "Design, Implementation, and evaluation of a Single-copy Protocol Stack" Software – Practice and Experience, Vol. 28, June 1998
AB		Meleis, Hanafy E.; Serpanos, Dimitrios, N., "Designing Communication Subsystems for High-Speed Networks" IEEE Network July 1992
Examiner	Alicia Baturay	
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	L							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
AB	R	Doumenis, Gr. A.; Reisis, D.I.; Stassinopoulos, G.I., "Efficient Implementation of the SAR Sublayer and the ATM Layer in High-Speed Broadband ISDN Data Terminal Adapters" IEEE 1993
AB	S	Mora, F.; Sebastia, A., "Electronic Design of a High Performance Interfacce to the SCI Network" IEEE 1998
AB	T	Eady, Fred, "Embedded Internet Part 2: TCP/IP and a 16-Bit Compiler" Embedded PC June 1999
AB		Shivam, Piyush; Wyckoff, Pete; Panda, Dhabaleswar, "EMP: Zero-copy OS-bypass NIC-driven Gigabit Ethernet" SC2001 November 2001, Denver CO, USA
AB		Mansour, Mohammad; Kayssi, Ayman, "FPGA-Based Internet Protocol Version 6 Router" IEEE 1998
AB		Smith, Jonathon M.; Traw, C. Brendan S., "Giving Applications Access to Gb/s Networking" IEEE Network July 1993
AB		Traw, C. Brendan S.; Smith, Jonathan M., "Hardware/Software Organization of a High-Performance ATM Host Interface" IEEE 1993
Examiner	Alicia Baturay	
Date Considered	10/11/2005	

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	L							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	R	Nagata, Takahiko; Hosoda, Yamashita, Hiroyuki, "High-Performance TCP/IP/ATM Communication Board" NTT Information and Communication Systems Laboratories 1998
AB	S	Nagata, Takahiko; Hosoda, Yasuhiro; Yamahsita, Hiroyuki, "High-Performance TCP/IP/ATM Communication Boards: Driving Force for Various Multimedia Services" Vol. 9 No. 6 November 1997
AB	T	Jolitz, William Frederick, "High-Speed Networking: Header prediction and forward-error correction for very high-speed data transfer" Dr. Dobbs Journal, August 1992
AB		Chiswell, Dave "Implementation Challenges for 155Mbit ATM Adapters" ISBN# 0-7803-2636-9
AB		Wright, Maury "Intelligent Ethernet Boards" EDN June 23, 1988
AB		Preston, David "Intetnet Protocols Migrate to Silicon For Networking Devices" Electronic Design April 14, 1997
Examiner	Date Considered	
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Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
AB	R	Ivanov-Loshkanov, V.S.; Sevast'yanov, S.F., Semenov, M.N., "Network Microprocessor Adapter" Avtmatika i Vyshislitel'naya Tekhnika Vol. 17 No. 5 pp. 25-28, 1983
AB	S	Druschel, Peter; Abbot, Mark B.; Pagels, Michael A.; Peterson, Larry L., "Network Subsystem Design" IEEE Network July 1993
AB	T	Huang, Jau-Hsiung; Chen, Chi-Wen, "On Performance Measurements of TCP/IP and its Device Driver" IEEE 1992
AB		Siegel, Martin; Williams, Mark; Robler, Georg, "Overcoming Bottlenecks in High-Speed Transport Systems" IEEE 1991
AB		Neufeld, Gerald W.; Ito, Mabo Robert; Goldberg, Murray; McCutcheon, Mark J.; Ritchie, Stuart, "Parallel Host Interface for an ATM Network" IEEE Network July 1993
AB		Maly, K.; Khanna, K.; Kukkamala, R.; Overstreet C.M.; Yerraballi, R.; Foundriat, E.C.; Madan, B., "Parallel TCP/IP For Multiprocessor Workstations" High Performance Netowrking, IV, 1993 IFIP
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	L							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
AB	R	Laskman, T.V.; Madhow, U., "Performance Analysis of Window-based Flow Control using TCP/IP: Effect of High Bandwidth Delay Products and Random Loss" High Performance Networking V. 1994 IFIP
AB	S	Ramakrishnan, K.K., "Performance Considerations in Designing Network Interfaces" IEEE Journal 1993
AB	T	Camarda, P.; Pipio, F.; Piscitelli, G.; "Performance evaluating of TCP/IP implementations in end systems" IEE Proc-Computer Digital Tech. Vol. 146 No. 1 January 1999
AB		Toyoshima, Kan; Shirakawa, Kazuhiro; Hayashi, Kazuhiro, "Programmable ATM Adapter: Rapid Prototyping of Cell Processing Equipment for ATM Network" IEEE 1997
AB		Blumrich, Matthias A.; Dubnicku, Cezary; Felton, Edward W.; Li, Kai, "Protected, User-level DMA for the SHRIMP Network Interface" IEEE 1996
AB		Feldmeier, David C.; McAuley, Anthony J.; Smith, Jonathan M., Bakin, Deborah S.; Marcus, William S.; Raleigh, Thomas M., "Protocol Boosters" IEEE 1998
Examiner	Date Considered	
Alicia Baturay	10/11/2005	

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty. Docket No.	Application No.:
	NVIDP322/P001314	10/049,972
	Applicant:	
	Michael Ward Johnson et al.	
Filing Date:	Group Art Unit:	
02/13/2002	-2661 2155	

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
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Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	L							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
AB	R	Marcus, William S.; Hadzic, Ilija; McAuley, Anthony J.; Smith, Jonathan M., "Protocol Boosters: Applying Programmability to Network Infrastructures" IEEE Communications Magazine October 1998
AB	S	Korablum, Deborah F., "Protocol Implementation and Other Performance Issues for Local and Metropolitan Area Networks" IEEE 1988
AB	T	Dittia, Zubin D.; Parulkar, Guru M.; Jr., Jerome R. Cox, "The APIC Approach to High Performance Network Interface Design: Protect4ed DMA and Other Techniques" IEEE 1997
AB		Rutsche, Erich, "The Architecture of a Gb/s Multimedia Protocol Adapter" ACM SIGCOMM Computer Communication Review
AB		Moldeklev, Kjersti; Klovning, Espen; Kure, Oivind, "The effect of end system hardware and software on TCP/IP throughput performance over a local ATM Network"
AB		Kanakia, Hermant; Cheriton, David R., "The VMP Network Adapter Board (NAB) High Performance Network Communication for Multiprocessors" ACM 1988
Examiner	<i>Alicia Bateman</i> Date Considered <i>10/11/2005</i>	

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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	NVIDP322/P001314	10/049,972
	Applicant:	
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Filing Date:	Group Art Unit:	
02/13/2002	2661 2155	

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A						
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Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	J							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
AB	K	Chandrammenon, Grish P.; Varghese, George, "Trading Packet Headers for Packet Processing" IEEE 1996
AB	L	Nielson, Dr. Michael J.K., "TURBOchannel" IEEE 1991
	M	New Media News, www.newmedianews.com/02197/ts_inettuner.html
AB	N	Kelly, T. "Cheap Internet Hardware that Fits in Everything" ZDNet, www.zdnet.co.uk/news/1998/77/ns-5998.html
AB	O	Kitadeya et al. "Matsushita Launches WebTV Internet Connection Terminal" www.mei.co.jp/corp/news/official.data.dir/en981112-1/en981112-1.html
AB	P	iReady Product Data Sheet, Internet Tuner
	Q	
Examiner	Date Considered	
<i>Alvin Bateman</i>	<i>10/11/2005</i>	

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Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty. Docket No. Application No.: NVIDP322/P001314 10/049,972 Applicant: Michael Ward Johnson et al. Filing Date: Group Art Unit: 02/13/2002 2155
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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
AB	A	5,012,489	4/30/1991	Burton et al.	375	8	11/7/1998
	B	5,161,193	11/3/1992	Lampson et al.	380	49	6/29/1990
	C	5,163,131	11/10/1992	Row et al.	395	200	9/8/1989
	D	5,303,344	4/12/1994	Yokoyama et al.	395	200	2/25/1991
	E	5,307,413	4/26/1994	Denzer	380	49	7/19/1991
	F	5,426,694	6/20/1995	Herbert	379	242	10/8/1993
	G	5,430,727	7/4/1995	Callon	370	85.13	5/19/1994
	H	5,440,551	8/8/1995	Suzuki	370	60	1/4/1994
	I	5,485,460	1/16/1996	Schrier et al.	370	94.1	8/19/1994
	J	5,495,480	2/27/1996	Yoshida	370	60	6/21/1994
	K	5,499,353	3/12/1996	Kadlec et al.	395	445	6/20/1995

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	L							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
AB	M	Chesson, Greg, "The Protocol Engine Project" Technology Focus September 1987
	N	Chesson, Greg, "Proceedings of the Summer 1987 USENIX Conference" USENIX Association June 8-12, 1987
	O	G. Chesson and L. Green, "XTP Protocol Engine VLSI for Real-Time LANS" EFOC/LAN June 29-July 1, 1968
	P	Stevens, Richard W., "TCP/IP Illustrated Volume" Addison-Wesley Professional Computing Series
	Q	Abbot, Mark B., and Peterson, Larry L., "Increasing Network Throughput by Integrating Protocol Layers" IEEE 1993
Examiner	<i>Alicia Bateman</i> Date Considered <i>10/11/2005</i>	

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



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Information Disclosure Statement By Applicant	Applicant: Michael Ward Johnson et al.	
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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
AB	A	5,513,324	4/30/1996	Dolin, Jr. et al.	395	200.18	3/7/1994
	B	5,519,704	3/21/1996	Farinacci et al.	370	85.13	4/21/1994
	C	5,546,453	8/13/1996	Herbert	379	242	6/7/1995
	D	5,566,170	10/15/1996	Bakke et al.	370	60	12/29/1994
	E	5,577,105	11/19/1996	Baum et al.	379	93	3/11/1994
	F	5,577,172	11/19/1996	Vatland et al.	395	114	8/31/1994
	G	5,577,237	11/19/1996	Lin	395	555	1/23/1995
	H	5,598,410	1/28/1997	Stone	370	469	12/29/1994
	I	5,619,650	4/8/1997	Bach et al.	395	200.01	9/21/1995
	J	5,625,678	4/29/1997	Blomfield-Brown	379	93	5/24/1995
	K	5,625,825	4/29/1997	Rostoker et al.	395	730	10/21/1993

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
	L						Yes	No

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
AB	M	Wright, Maury, "Low-Cost Control LANs Add Automation to Homes, Autos, and Offices" EDN July 20, 1992
	N	Preston, David J., "Internet Protocols Migrate to Silicon For Networking Devices" Electronic Design April 14, 1997
	O	Muller, Raimund, "LON - das universelle Netzwerk" Elektronik 22/1991
AB	P	Rang, Michael ad Tantawy, Ahmed, "A Design Methodology for Protocol Processors" IEEE 1995
Examiner	Date Considered	
Alicia Baturay	10/11/2005	

Not in English

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



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	NVIDP322/P001314	10/049,972
	Applicant:	
	Michael Ward Johnson et al.	
	Filing Date:	Group Art Unit:
	02/13/2002	2661

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
AB	A	5,634,015	5/27/1997	Chang et al.	395	309	10/4/1994
	B	5,636,371	6/3/1997	Yu	395	500	6/7/1995
	C	5,640,394	6/17/1997	Schrier et al.	370	389	1/16/1996
	D	5,663,951	9/2/1997	Danneels et al.	370	230	6/6/1995
	E	5,666,362	9/9/1997	Chen et al.	370	420	7/25/1995
	F	6,575,507	10/7/1997	Bobo, II	364	514R	4/28/1995
	G	5,687,314	11/11/1997	Osman et al.	395	200.01	6/9/1992
	H	5,696,899	12/9/1997	Kalwitz	395	200.1	11/18/1992
	I	5,699,350	12/16/1997	Kraslavsky	370	254	10/6/1995
	J	5,701,316	12/23/1997	Alferness et al.	371	53	8/31/1995
	K	5,727,149	3/10/1998	Hirata et al.	395	200.8	12/21/1995

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
	L						Yes	No

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
AB	M	Banks, David and Prudence, Michael, "A High-Performance Network Architecture for a PA-RISC Workstation" IEEE Journal Vol. II, No. 22 February 1993
	N	Steenkiste, Peter, "A High-Speed Network Interface for Distributed-Memory Systems: Architecture and Applications" ACM Transactions on Computer Systems, Vol. 15, No. 1 February 1997
	O	Doumenis, Gr.A., Konstantoulakis, G.E., Reisis, D.I. and Stassinopoulos, G.I. "A Personal Computer Hosted Terminal Adapter For The Broadband Integrated Services Digital Network and Applications" National Technical University of Athens, Greece
Examiner	Date Considered	
Alicia Bateman	10/11/05	

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



Form 1449 (Modified)	Atty. Docket No. NVIDP322/P001314	Application No.: 10/049,972
Information Disclosure Statement By Applicant	Applicant: Michael Ward Johnson et al.	
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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
AG	A	5,734,865	3/31/1998	Yu	395	500	6/27/1995
	B	5,748,905	5/5/1998	Hauser et al.	395	200.79	8/30/1996
	C	5,754,540	5/19/1998	Liu et al.	370	315	7/18/1995
	D	5,761,281	6/2/1998	Baum et al.	379	93.29	7/30/1996
	E	5,790,546	8/4/1998	Dobbins et al.	370	400	12/4/1995
	F	5,790,676	8/4/1998	Ganesan et al.	380	23	9/23/1997
	G	5,802,278	9/1/1998	Isfeld et al.	395	200.02	1/23/1996
	H	5,802,306	9/1/1998	Hunt	395	200.58	10/31/1995
	I	5,805,816	9/8/1998	Picazo, Jr. et al.	395	200.53	1/28/1997
	J	5,809,235	9/15/1998	Sharma et al.	395	200.6	3/8/1996
	K	5,815,516	9/29/1998	Aaker et al.	371	53	4/5/1996

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation
	L						Yes No

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
AB	M	Womack, Lucas; Mraz, Ronald; Mendelson, Abraham, "A Study of Virtual Memory MTU Reassembly withing the PowerPC Architecture" IEEE 1997
	N	Steenkiste, Peter, "A Systematic Approach to Host Interface Design for High-Speed Networks" IEEE March 1994
	O	Wittie, Larry D., Ma, Fanyuan, "A TCP/IP Communication Subsystem in Micros" IEEE 1987
Examiner	<i>Alicia Bateman</i>	
	Date Considered	10/11/2005

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



Form 1449 (Modified)	Atty. Docket No. NVIDP322/P001314	Application No.: 10/049,972
Information Disclosure Statement By Applicant	Applicant: Michael Ward Johnson et al.	
(Use Several Sheets if Necessary)	Filing Date: 02/13/2002	Group Art Unit: 2661

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
AB	A	5,818,935	10/6/1998	Maa	380	20	3/10/1997
	B	5,826,032	10/20/1998	Finn et al.	395	200.66	2/12/1996
	C	5,872,919	2/16/1999	Wakeland	395	200.6	5/7/1997
	D	5,894,557	4/13/1999	Bade et al.	395	200.58	3/29/1996
	E	5,909,546	6/1/1999	Osborne	395	200.42	3/8/1996
	F	5,920,732	7/6/1999	Riddle	395	876	7/1/1996
	G	5,935,268	8/10/1999	Weaver	714	758	6/3/1997
	H	5,937,169	8/10/1999	Connery et al.	395	200.8	10/29/1997
	I	5,941,988	8/24/1999	Bhagwat et al.	713	201	1/27/1997
	J	5,943,481	8/24/1999	Wakeland	395	200.6	5/7/1997
	K	5,974,518	10/26/1999	Nogradi	711	173	4/10/1997

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
	L						Yes	No

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
AB	M	Dalton, Chris; Watson, Greg; Banks, David; Calamvokis, Costas; Edwards, Aled; Lumley, John, "Afterburner: A Network-independent card provides architectural support for high-performance protocols" IEEE July 1993
	N	Gupta, Pankaj; McKeown, Nick, "Algorithms for Packet Classification" IEEE Network March/April 2001
	O	Clark, David D.; Romkey, John; Salwen, Howard, "An Analysis of TCP Processing Overhead" IEEE 1988
Examiner <i>Alicia Bateman</i>		Date Considered <i>10/11/2005</i>

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty. Docket No.	Application No.:
	NVIDP322/P001314	10/049,972
	Applicant:	
	Michael Ward Johnson et al.	
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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
AB	A	5,999,974	12/7/1999	Ratcliff et al.	709	224	8/29/1997
	B	6,014,699	1/11/2000	Ratcliff et al.	709	224	8/29/1997
	C	6,034,963	3/7/2000	Minami et al.	370	401	10/31/1996
	D	6,046,980	4/4/2000	Packer	370	230	11/24/1997
	E	6,061,742	5/9/2000	Stewart et al.	709	250	10/10/1997
	F	6,076,115	6/13/2000	Sambamurthy et al.	709	250	4/24/1997
	G	6,081,846	6/27/2000	Hyder et al.	709	250	5/8/1997
	H	6,092,110	7/18/2000	Maria et al.	709	225	10/23/1997
	I	6,092,229	7/18/2000	Boyle et al.	714	748	10/9/1996
	J	6,098,188	8/1/2000	Kalmanek, Jr. et al.	714	746	6/30/1995
	K	6,101,543	8/8/2000	Alden et al.	709	229	10/25/1996

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
	L						Yes	No

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
AB	M	Clark, David D.; Jacobson, Van; Romkey, John; Salwen, Howard, "An Analysis of TCP Processing Overhead" IEEE June 1989
	N	Goloi, W.K.; Behr, P. "An IPC Protocol and Its Hardware Realization For A High-Speed Distributed Multicomputer System" IEEE 1981
	O	Ames, Richard, "Building an Embedded Web Server from Scratch" Circuit Cellar INK February 1998
Examiner	<i>Alicia Baturay</i>	
	Date Considered	10/11/2005

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



Form 1449 (Modified)	Atty. Docket No. NVIDP322/P001314	Application No.: 10/049,972
Information Disclosure Statement By Applicant	Applicant: Michael Ward Johnson et al.	
(Use Several Sheets if Necessary)	Filing Date: 02/13/2002	Group Art Unit: 2661

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
AB	A	6,151,625	11/21/2000	Swales et al.	709	218	4/30/1999
	B	6,157,955	12/5/2000	Narad et al.	709	228	6/15/1998
	C	6,172,980	1/9/2001	Flanders et al.	370	401	9/11/1997
	D	6,172,990	1/9/2001	Deb et al.	370	474	11/12/1997
	E	6,173,333	1/9/2001	Jolitz et al.	709	240	1/28/1999
	F	6,182,228	1/30/2001	Boden et al.	713	201	8/17/1998
	G	6,185,619	2/6/2001	Joffe et al.	709	229	11/7/1997
	H	6,230,193	5/8/2001	Arunkumar et al.	709	218	10/31/1996
	I	6,233,626	5/15/2001	Swales et al.	710	11	10/6/1998
	J						
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Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation
	L						Yes No

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
AB	M	Legg, John, "Choosing and implementing an embedded TCP/IP Stack" Electronic Product Design January 1999
	N	Orphanos, George; Birbas, Alexios; Petrellis, Nikos; Mountzouris, Ioannis; Malataras, Andreas, "Compensating for Moderate Effective Throughput at the Desktop" IEEE Communication Magazine April 2000
	O	Yocum, Kenneth G.; Chase, Jeffrey S.; Gallatin, Andrew J.; Lebeck, Alvin R., "Cut-Through Delivery in Trapeze: An Exercise in Low-Latency Messaging IEEE 1997
Examiner	Date Considered	
Alicia Bateman	10/11/2005	

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



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	02/13/2002	2661

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub- class	Filing Date
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Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub- class	Translation	
							Yes	No
	B							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
AB	C	Varada, S.; Yang, Y.; Evans, D., "Data and Buffer Management in ATM Systems" TranSwitch Corporation
	D	Bonjour, Dominique; de Hauteclocque, Gaelle; le Moal, Jacques, "Design and Application of ATM LAN/WAN Adapters" IEEE 1998
	E	Kim, Chan; Jun, Jong-Jun; Park, Yeong-Ho; Lee, Kyu-Ho; Kim, Hyup-Jong, "Design and Implementation of a High-Speed ATM Host Interface Controller" Electronics and Telecommunications Research Institute, Korea
	F	Steenkiste, Peter, "Design, Implementation, and evaluation of a Single-copy Protocol Stack" Software – Practice and Experience, Vol. 28, June 1998
	G	Meleis, Hanafy E.; Serpanos, Dimitrios, N., "Designing Communication Subsystems for High-Speed Networks" IEEE Network July 1992
Examiner	Date Considered	
Alicia Baturay		10/11/2005

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
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Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	B							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
AB	C	Doumenis, Gr. A.; Reisis, D.I.; Stassinopoulos, G.I., "Efficient Implementation of the SAR Sub layer and the ATM Layer in High-Speed Broadband ISDN Data Terminal Adapters" IEEE 1993
	D	Mora, F.; Sebastia, A., "Electronic Design of a High Performance Interface to the SCI Network" IEEE 1998
	E	Eady, Fred, "Embedded Internet Part 2: TCP/IP and a 16-Bit Compiler" Embedded PC June 1999
	F	Shivam, Piyush; Wyckoff, Pete; Panda, Dhableswar, "EMP: Zero-copy OS-bypass NIC-driven Gigabit Ethernet" SC2001 November 2001, Denver CO, USA
	G	Mansour, Mohammad; Kayssi, Ayman, "FPGA-Based Internet Protocol Version 6 Router" IEEE 1998
	H	Smith, Jonathon M.; Traw, C. Brendan S., "Giving Applications Access to Gb/s Networking" IEEE Network July 1993
	I	Traw, C. Brendan S.; Smith, Jonathan M., "Hardware/Software Organization of a High-Performance ATM Host Interface" IEEE 1993
Examiner	Alicia Baturay	
	Date Considered	10/11/2005

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant



Form 1449 (Modified)	Atty. Docket No. NVIDP322/P001314	Application No.: 10/049,972
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U.S. Patent Documents

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Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub- class	Translation	
							Yes	No
	B							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	C	Nagata, Takahiko; Hosoda, Yamashita, Hiroyuki, "High-Performance TCP/IP/ATM Communication Board" NTT Information and Communication Systems Laboratories 1998
AB	D	Nagata, Takahiko; Hosoda, Yasuhiro; Yamahsita, Hiroyuki, "High-Performance TCP/IP/ATM Communication Boards: Driving Force for Various Multimedia Services" Vol. 9 No. 6 November 1997
	E	Jolitz, William Frederick, "High-Speed Networking: Header prediction and forward-error correction for very high-speed data transfer" Dr. Dobbs Journal, August 1992
	F	Chiswell, Dave "Implementation Challenges for 155Mbit ATM Adapters" ISBN# 0-7803-2636-9
	G	Wright, Maury "Intelligent Ethernet Boards" EDN June 23, 1988
	H	Preston, David "Internet Protocols Migrate to Silicon For Networking Devices" Electronic Design April 14, 1997
Examiner <i>Alicia Baturay</i>		Date Considered <i>10/11/2005</i>

Not in
English

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub- class	Filing Date
	A						

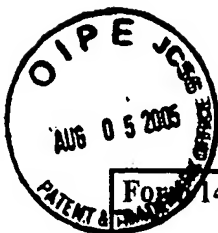
Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub- class	Translation	
							Yes	No
	B							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
AB	C	Ivanov-Loshkanov, V.S.; Sevast'yanov, S.F., Semenov, M.N., "Network Microprocessor Adapter" Avtmatika i Vyshislitel'naya Tekhnika Vol. 17 No. 5 pp. 25-28, 1983
	D	Druschel, Peter; Abbot, Mark B.; Pagels, Michael A.; Peterson, Larry L., "Network Subsystem Design" IEEE Network July 1993
	E	Huang, Jau-Hsiung; Chen, Chi-Wen, "On Performance Measurements of TCP/IP and its Device Driver" IEEE 1992
	F	Siegel, Martin; Williams, Mark; Robler, Georg, "Overcoming Bottlenecks in High-Speed Transport Systems" IEEE 1991
	G	Neufeld, Gerald W.; Ito, Mabo Robert; Goldberg, Murray; McCutcheon, Mark J.; Ritchie, Stuart, "Parallel Host Interface for an ATM Network" IEEE Network July 1993
	H	Maly, K.; Khanna, K.; Kukkamala, R.; Overstreet C.M.; Yerraballi, R.; Foundriat, E.C.; Madan, B., "Parallel TCP/IP For Multiprocessor Workstations" High Performance Networking, IV, 1993 IFIP
Examiner	Date Considered	
Alicia Baturay	10/11/2005	

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



Form 1449 (Modified)	Atty. Docket No. NVIDP322/P001314	Application No.: 10/049,972
Information Disclosure Statement By Applicant	Applicant: Michael Ward Johnson et al.	
(Use Several Sheets if Necessary)	Filing Date: 02/13/2002	Group Art Unit: 2661

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub- class	Filing Date
	A						

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub- class	Translation	
							Yes	No
	B							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
AB	C	Laskman, T.V.; Madhow, U., "Performance Analysis of Window-based Flow Control using TCP/IP: Effect of High Bandwidth Delay Products and Random Loss" High Performance Networking V. 1994 IFIP
AB	D	Ramakrishnan, K.K., "Performance Considerations in Designing Network Interfaces" IEEE Journal 1993
AB	E	Camarda, P.; Pipio, F.; Piscitelli, G.; "Performance evaluating of TCP/IP implementations in end systems" IEE Proc-Computer Digital Tech. Vol. 146 No. 1 January 1999
	F	Toyoshima, Kan; Shirakawa, Kazuhiro; Hayashi, Kazuhiro, "Programmable ATM Adapter: Rapid Prototyping of Cell Processing Equipment for ATM Network" IEEE 1997
	G	Blumrich, Matthias A.; Dubnicku, Cezary; Felton, Edward W.; Li, Kai, "Protected, User-level DMA for the SHRIMP Network Interface" IEEE 1996
	H	Feldmeier, David C.; McAuley, Anthony J.; Smith, Jonathan M.; Bakin, Deborah S.; Marcus, William S.; Raleigh, Thomas M., "Protocol Boosters" IEEE 1998
Examiner	Date Considered	
Alicia Baturay	10/11/2005	

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							Yes	No
	B							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
AB	C	Marcus, William S.; Hadzic, Ilija; McAuley, Anthony J.; Smith, Jonathan M., "Protocol Boosters: Applying Programmability to Network Infrastructures" IEEE Communications Magazine October 1998
	D	Korablum, Deborah F., "Protocol Implementation and Other Performance Issues for Local and Metropolitan Area Networks" IEEE 1988
	E	Dittia, Zubin D.; Parulkar, Guru M.; Jr., Jerome R. Cox, "The APIC Approach to High Performance Network Interface Design: Protect4ed DMA and Other Techniques" IEEE 1997
	F	Rutsche, Erich, "The Architecture of a Gb/s Multimedia Protocol Adapter" ACM SIGCOMM Computer Communication Review
	G	Moldeklev, Kjersti; Klovning, Espen; Kure, Oivind, "The effect of end system hardware and software on TCP/IP throughput performance over a local ATM Network"
	H	Kanakia, Hermant; Cheriton, David R., "The VMP Network Adapter Board (NAB) High Performance Network Communication for Multiprocessors" ACM 1988
Examiner	Date Considered	
Alicia Baturay	10/11/2005	

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



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	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation
	J						Yes No

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
AB	K	Chandrammenon, Grish P.; Varghese, George, "Trading Packet Headers for Packet Processing" IEEE 1996
	L	Nielson, Dr. Michael J.K., "TURBOchannel" IEEE 1991
	M	New Media News, www.newmedianews.com/02197/ts_inettuner.html
	N	Kelly, T. "Cheap Internet Hardware that Fits in Everything" ZDNet, www.zdnet.co.uk/news/1998/77/ns-5998.html
	O	iReady Product Data Sheet, Internet Tuner
	P	Kitadeya et al. "Matsushita Launches WebTV Internet Connection Terminal" www.mei.co.jp/corp/news/official.data.dir/en981112-1/en981112-1.html
	Q	
Examiner <i>Alicia Bortunay</i>	Date Considered <i>10/11/2005</i>	

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